FIG. 1

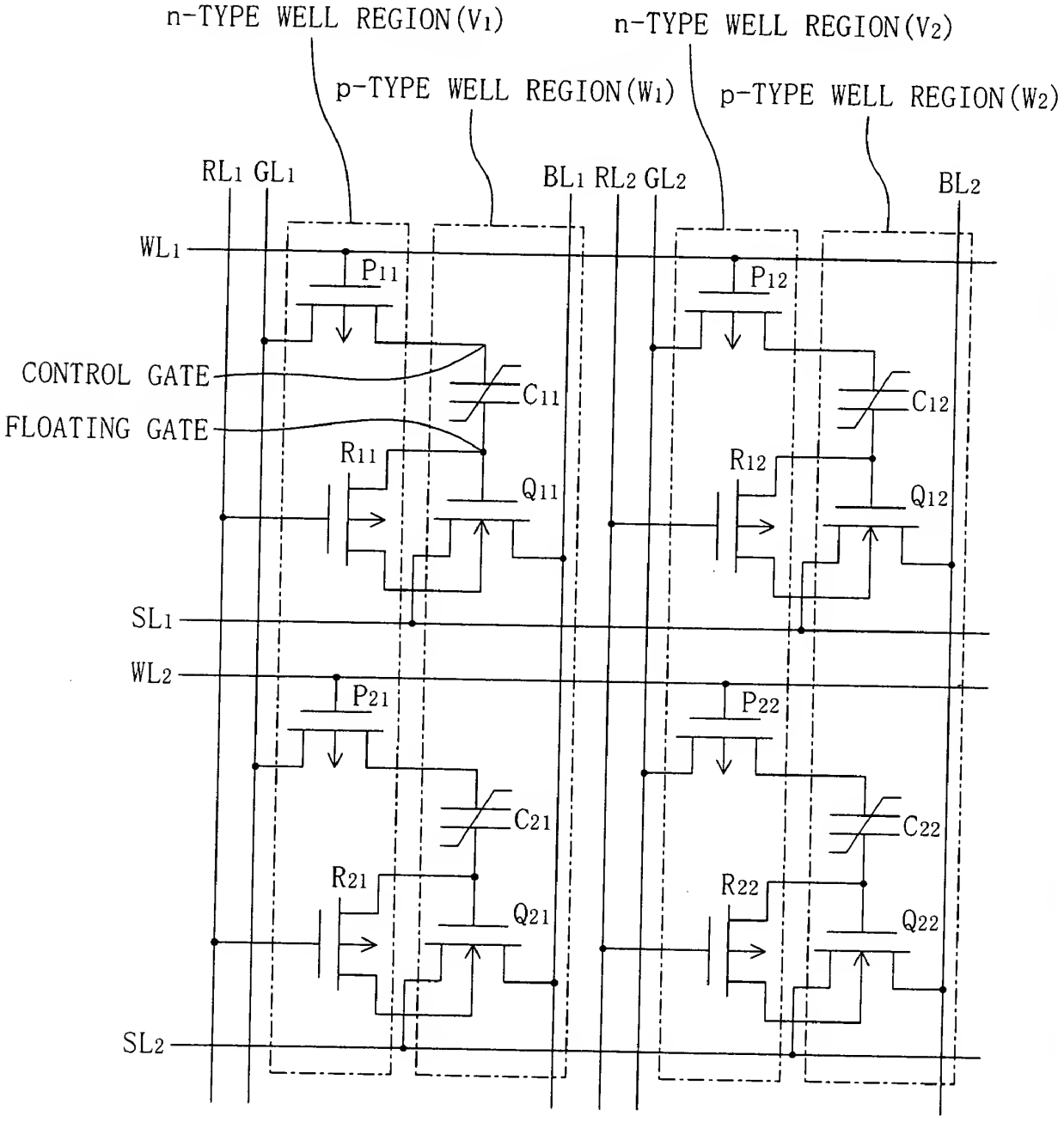

n-TYPE WELL REGION(V1)
n-TYPE WELL REGION(V2)
p-TYPE WELL REGION(W1)
p-TYPE WELL REGION(W2)
RL1 GL1
BL1 RL2 GL2
BL2
WL1
WL2
SL1
SL2
CONTROL GATE
FLOATING GATE
P11
P12
P21
P22
C11
C12
C21
C22
R11
R12
R21
R22
Q11
Q12
Q21
Q22

FIG. 2

	OPERATION	
	WRITE	READ
WL_1	0	0
SL_1	0	0
GL_1	$\pm V_p$	V_r
BL_1	0	V_d
RL_1	0	V_p
V_1	V_p	V_p
W_1	0	0
WL_2	V_p	V_p
SL_2	0	0
GL_2	0	0
BL_2	0	0
RL_2	V_p	V_p
V_2	V_p	V_p
W_2	0	0

FIG. 3

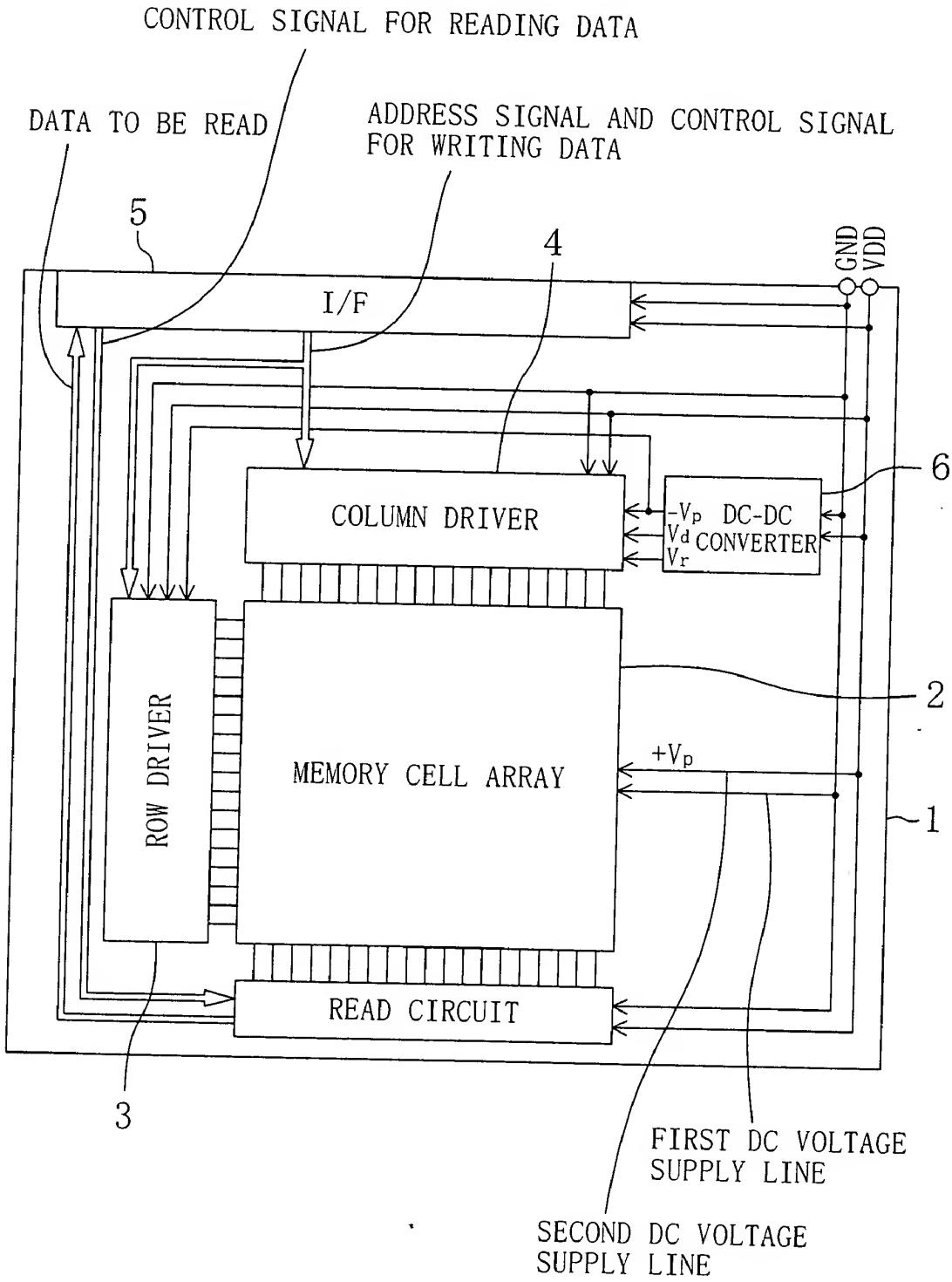
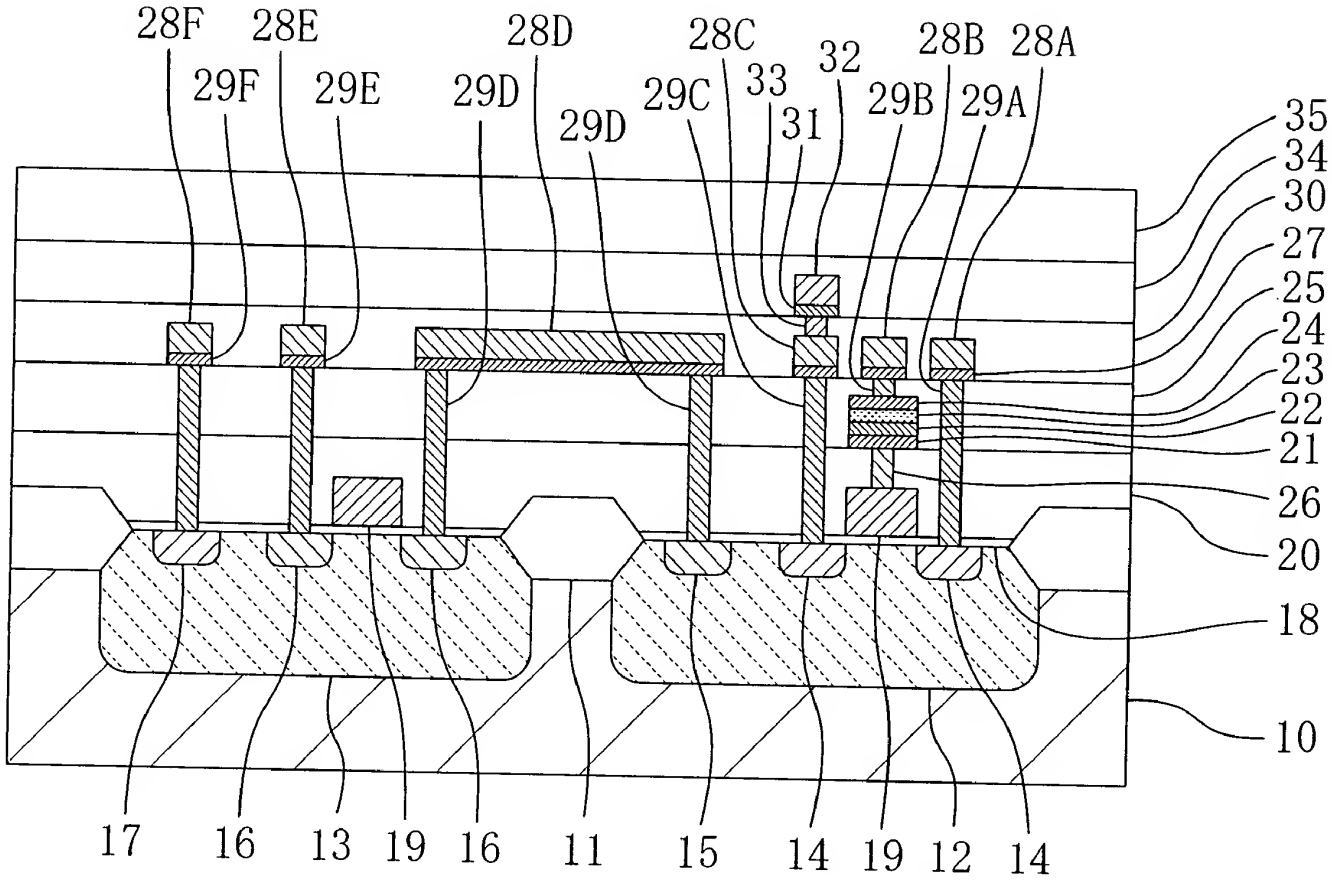

CONTROL SIGNAL FOR READING DATA
DATA TO BE READ
ADDRESS SIGNAL AND CONTROL SIGNAL FOR WRITING DATA
5
4
GND
VDD
I/F
6
COLUMN DRIVER
-Vp DC-DC
Vd CONVERTER
Vr
2
ROW DRIVER
MEMORY CELL ARRAY
+Vp
1
READ CIRCUIT
3
FIRST DC VOLTAGE SUPPLY LINE
SECOND DC VOLTAGE SUPPLY LINE

FIG. 4


28F
28E
28D
28C
32
28B
28A
29F
29E
29D
29D
29C
33
31
29B
29A
35
34
30
27
25
24
23
22
21
26
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18
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16
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14
19
12
14

FIG. 5A
PRIOR ART

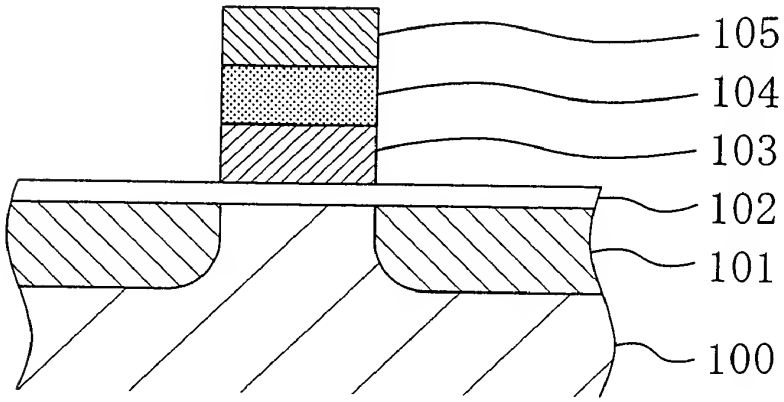
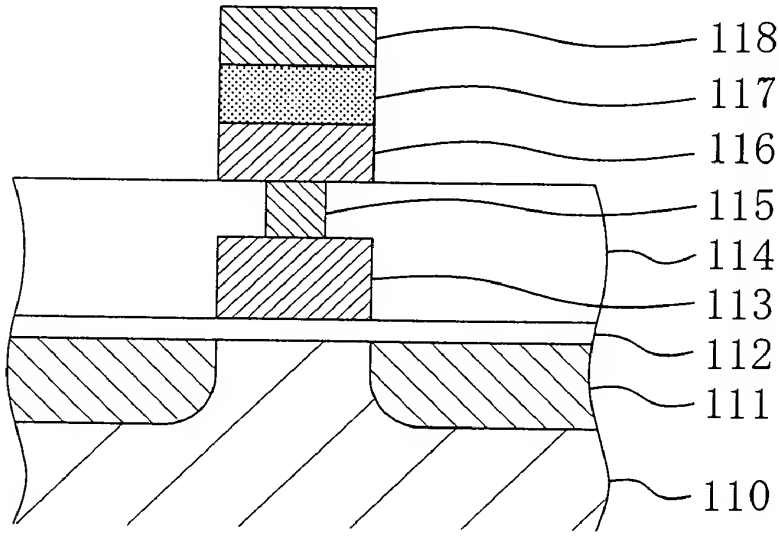
FIG. 5B
PRIOR ART